Seventh full paragraph on page 19 now reads as follows:

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Fig. 8 is a diagram referenced for describing impedance between high efficiency amplifier 1A and an isolator 3A;

## **IN THE CLAIMS:**

Claim 1 now reads as follows:

1. (Amended) A high efficiency amplifier, connected to a non-reciprocal circuit element having an input impedance lower than a standard impedance and an output impedance substantially equal to said standard impedance, comprising:

an input terminal to receive an input signal;

an output terminal connected to said non-reciprocal circuit element; an amplifier element to amplify said input signal; and

one or a plurality of harmonic processing circuits arranged between said amplifier element and said output terminal to process a harmonic in an output signal of said amplifier element.

## **REMARKS**

Claims 1-6, 8, 33-38, and 40 are presented for examination.

**OBJECTIONS** 

Claim 1 has been objected to because the Examiner takes the position that the claim language contradicts to the specification.

In response, claim 1 has been corrected by deleting "," in line 2. Hence, claim 1 indicates that the non-reciprocal circuit element, <u>not the high efficiency amplifier</u>, has "an